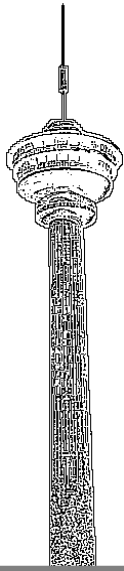


Call for Papers

SoC 2003

The International Symposium on System-on-Chip
Tampere, Finland November 19-21, 2003



Organizer: *Institute of Digital and Computer Systems at Tampere University of Technology*
Sponsored by: *Nokia Corporation*
Technical co-sponsor: *IEEE Circuits and Systems Society*
In co-operation with: *The European Commission IST program (SoC-Mobinet project) and national TELESOC graduate school*

SoC 2003 builds on the tradition of a series of SoC events organized in Tampere, Finland annually since 1999. In addition to the invited lectures and commercial exhibit with its own program track, the conference is now open for contributions from researchers on this broad but focused field. The symposium also features one or several panel discussions on topics of high interest to the community. The mission of SoC 2003 is to provide a forum that is fully and comprehensively dedicated to SoC.

General Chair:

Jari Nurmi, TUT, Finland

Scientific Program Chair:

Jarmo Takala, TUT, Finland

Industry Program Chair:

Timo D. Hämäläinen, TUT, Finland

Scientific Program Committee:

Wael Badawy, University of Calgary, Canada

Henk Corporaal, IMEC, Belgium

Steven Guccione, QuickSilver Technology, USA

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Rainer Leupers, RWTH Aachen, Germany

Oz Levia, Improv Systems, USA

Jan Madsen, Technical University of Denmark

Heinrich Meyr, RWTH Aachen, Germany

Keshab K. Parhi, University of Minnesota, USA

Jan Rabaey, UC Berkeley, USA

Stefan Rusu, Intel, USA

Satnam Singh, Xilinx, USA

Risto Suoranta, Nokia, Finland

Hannu Tenhunen, Royal Institute of Technology, Sweden

Lionel Torres, LIRMM, France

Stamatis Vassiliadis, Delft University of Technology, the Netherlands

We encourage your contribution to the program by submitting a research paper, suggesting a distinguished invited lecturer and/or topic, submitting an abstract to the industry program track, suggesting a tutorial course, or a topic and tentative participants for a panel discussion.

Topics of interest include, but are not limited to:

- *Alternative computing paradigms*
- *Analysis and early estimation techniques, technology roadmaps*
- *Application-specific processors and architectures*
- *Configurable and reconfigurable architectures*
- *Design flow and methodology*
- *Embedded processor hardware*
- *Embedded software tools and techniques, e.g. retargetable compilers*
- *Engineering education to qualify for SoC*
- *Legal aspects and business opportunities*
- *Low-power techniques*
- *Multiprocessor SoC*
- *Network-on-Chip*
- *On-chip communication and interconnects*
- *Physical design issues*
- *Platform architectures*
- *Reuse techniques*
- *SoC applications*
- *System-level integration*
- *Tools and languages for SoC design*
- *Verification, debugging, testing and testability*

All topics should be handled **highlighting the relevance to SoC**.

Submission Due Dates:

Invited lectures and scientific papers: May 30, 2003.

Tutorials: June 30, 2003.

Industrial abstracts and panels: August 29, 2003.

All submissions must be made electronically in PDF format.

Additional submission instructions are available on the conference website.

Complete details about the submission, author schedules, registration, program and venue are available on the symposium website: <http://www.cs.tut.fi/soc/>

