

SoC 2002 Tampere Hall November 20-21, 2002

Agenda

Wednesday, November 20

09:15 - 10:00 registration and coffee
10:00 - 10:15 opening
10:15 - 11:00 Dynamic Reconfigurable Systems: Blending HW and SW
Diederik Verkest, IMEC
11:00 - 11:30 coffee break (*Celoxica* vendor presentation)
11:30 - 12:15 Application-specific Multiprocessor SoC
Ahmed Jerraya, INPG
12:15 - 13:30 lunch
13:30 - 14:00 coffee at the exhibit (*AXYS Design Automation* presentation)
14:00 - 14:45 A Reconfigurable Processor and Software Environment for High
Performance Embedded Systems **Fabio Campi, U Bologna**
14:45 - 15:15 coffee break (*Tensilica* presentation)
15:15 - 16:00 Compiler Design for Modern Embedded Processor Architectures
Rainer Leupers, RWTH Aachen
16:00 - 16:30 coffee break (*PACT XPP* presentation)
16:30 - 18:30 panel discussion
18:45 bus transportation to banquet
19:00 - 22:00 banquet at restaurant Finlayson Palace

Thursday, November 21

09:00 - 09:45 Reconfigurable Microcoded Processors
Stamatis Vassiliadis, Delft U
09:45 - 10:15 coffee break (*LISATek* presentation)
10:15 - 11:00 Designing Networks on Chips: Solutions and Challenges
Luca Benini, U Bologna
11:00 - 11:30 coffee break (*Mentor Graphics* presentation)
11:30 - 12:15 Stream Computations Organized for Reconfigurable Execution
(SCORE) **Eylon Caspi, UC Berkeley**
12:15 - 13:30 lunch
13:30 - 14:15 System-level Specification for Platform FPGAs
Satnam Singh, Xilinx
14:15 - 14:45 coffee break (*Xilinx* presentation)
14:45 - 15:30 In Pursuit of Equal Opportunity Representation for Our Complex
Neighbors... **Tariq Jamil, Sultan Qaboos U**
15:30 - 17:00 coffee at posters (*Altera* and *Nordic VLSI* presentations)
SoC research posters by TUT and its university partners
17:00 - 17:15 wrap-up and closing
17:15 bus transportation (downtown, post-seminar event)
17:30 - 21:00 post-seminar event (restaurant Plevna, a simple dinner)