

Multipath mitigation performance of multi-correlator based code tracking algorithms in closed and open loop model

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Abstract—Multipath is one of the dominant error sources in satellite-based positioning. It is a well known fact that the conventional code tracking loop suffers from performance degradation due to the presence of multipath. In past years, several advanced signal processing techniques have been devised to mitigate the multipath induced errors. One particular class of these signal processing techniques is the multi-correlator based feed-forward approach. The multipath performance of these feed-forward techniques has been extensively studied in open loop configuration by the authors in [1], [9]. The goal of this paper is to analyze the performance of these multi-correlator based feed-forward techniques in closed loop configuration, i.e., in the presence of an NCO and a loop filter, in multipath channels. Additionally, the delay tracking methods previously studied for BPSK and Sine BOC signals are also studied here with the newly proposed Multiplexed BOC modulation.

I. INTRODUCTION

Multipath signal propagation remains a dominant source of error in Global Navigation Satellite System (GNSS) positioning. In the past years, lots of work have been achieved to improve the multipath rejection performance of the receivers. In order to reduce multipath error due to the presence of reflected signals, several approaches have been used. Among them, the use of special multipath limiting antennas (i.e., choke ring or multi-beam antennas), the post-processing techniques to reduce carrier multipath, the carrier smoothing to reduce code multipath, and the code tracking algorithms based on receiver internal correlation technique (i.e., narrow Early-Minus-Late [2] or High Resolution Correlator [10]) are the most prominent approaches. In this paper, our focus is limited to the correlation based multipath mitigation techniques. The most known code tracking algorithm is the traditional Early-Minus-Late (EML), which is composed of 1 chip spacing between early and late correlator pair. The traditional EML has limited multipath mitigation capability, and therefore, several enhanced EML-based techniques have been introduced, specially to mitigate closely spaced multipath. One class of these enhanced EML techniques is based on the idea of narrowing the spacing between the early and late correlators, i.e., narrow EML (nEML), provided that sufficient front-end bandwidth is assured [2]. Another enhanced version of this type of structure is the High Resolution Correlator (HRC), that uses an increased number of correlators for better coping

with medium-to-long delay multipath [5], [10].

Alternatively, several feed-forward techniques have been introduced in the literature in past few years [1], [9]. While improving the delay estimation accuracy, these techniques require a larger number of correlators than the traditional Delay Locked Loop (DLL), and they are sensitive to the noise-dependent threshold choice. Among the feed-forward techniques, two most competitive ones, previously proposed by the authors, are selected herein for performance comparison. These are Peak Tracking, based on 2^{nd} order Differentiation (PT(Diff2)), and Teager-Kaiser (TK), the details of which can be obtained in [1]. The performance of these multi-correlator based techniques [1], [4], [9] have been extensively studied in fading multipath environment with open loop model, i.e., in the absence of a Numerically Controlled Oscillator (NCO) and a loop filter. In this paper, we present the performance of these multi-correlator based tracking algorithms in closed loop model, i.e., in the presence of an NCO and a loop filter, in multipath channels. The main novelty of this paper comes from building the link between feed-forward and feedback approaches, and showing that the feed-forward approaches previously proposed have a great potential also when used in closed loop configuration. Additionally, the delay tracking methods previously studied for Binary Phase Shift Keying (BPSK) and Sine Binary Offset Carrier (SinBOC) modulated signals are also studied here with the newly proposed Multiplexed Binary Offset Carrier (MBOC) modulation.

This paper is organized as follows. The overview of MBOC modulation is presented in Section II, followed by a description about the implemented closed loop model in Section III. The multipath tracking performance via semi-analytical Multipath Error Envelopes (MEE) in closed loop model are presented in Section IV. The simulation results for both closed and open loop models are shown in Sections V and VI, respectively. Finally, section VII draws some general conclusions based on the obtained results.

II. OVERVIEW OF MBOC MODULATION

The GIOVE-B, the second Galileo satellite, launched on April 27, 2008 started transmitting the Galileo L1 signal using a specific optimized wave-form, MBOC, that will be interoperable with the L1C signal to be used in future Block III

GPS satellites, in accordance with the July 2007 agreement between the European Union and the United States [11]. The MBOC modulation enables receivers to obtain significantly better multipath mitigation performance than BPSK and SinBOC(1,1) modulations. The multipath improvement of MBOC modulation over SinBOC(1,1) is shown in [11] with the transmitted GIOVE-B signal.

The MBOC(6,1,1/11) power spectral density is a mixture of SinBOC(1,1) and SinBOC(6,1) spectra. The MBOC(6,1,1/11) spectrum can be generated by a number of different time waveforms that allows flexibility in implementation. The Time-Multiplexed BOC (TMBOC) implementation interlaces SinBOC(6,1) and SinBOC(1,1) spreading symbols in a regular pattern, whereas Composite BOC (CBOC) uses multilevel spreading symbols formed from the weighted sum (or difference) of SinBOC(1,1) and SinBOC(6,1) spreading symbols, interplexed to form a constant modulus composite signal [3]. Following the BOC model and derivations of [7], the composite CBOC signal which is used here can be written as:

$$\begin{aligned} s_{CBOC}(t) &= w_1 s_{SinBOC(1,1),held}(t) \pm w_2 s_{SinBOC(6,1)}(t) \\ &= w_1 \sum_{i=0}^{N_{B1}-1} \sum_{k=0}^{N_{B2}-1} (-1)^i c \left(t - i \frac{T_c}{N_{B1}} - k \frac{T_c}{N_{B2}} \right) \\ &\pm w_2 \sum_{i=0}^{N_{B2}-1} (-1)^i c \left(t - i \frac{T_c}{N_{B2}} \right) \end{aligned} \quad (1)$$

Above, when the 2 right-hand terms are added, additive CBOC or CBOC(‘+’) is formed; when the 2 terms are subtracted, we have the inverse CBOC or CBOC(‘-’) implementation. Alternatively, CBOC(‘+/-’) implementation can be used, when odd chips are CBOC(‘+’) modulated and even chips are CBOC(‘-’) modulated [3]. In eqn. 1, $N_{B1} = 2$ is the BOC modulation order for SinBOC(1,1) signal, $N_{B2} = 12$ is the BOC modulation order for SinBOC(6,1) signal, the term $s_{SinBOC(1,1),held}$ represents that SinBOC(1,1) signal is passed through a hold clock in order to match the higher rate of SinBOC(6,1); and w_1 and w_2 are amplitude weighting factors such that $w_1 = \sqrt{10/11} = 0.9535$ and $w_2 = \sqrt{1/11} = 0.3015$, and $c(t)$ is the pseudorandom code as defined in eqn. 2.

$$c(t) = \sqrt{E_b} \sum_{n=-\infty}^{\infty} b_n \sum_{m=1}^{S_F} c_{m,n} p_{T_{B2}}(t - nT_c S_F - mT_c), \quad (2)$$

where b_n is the n -th code symbol, E_b is the code symbol energy, S_F is the spreading factor or number of chips per code symbol ($S_F = 1023$), $c_{m,n}$ is the m -th chip corresponding to the n -th symbol, T_c is the chip rate, and $p_{T_{B2}}(\cdot)$ is a rectangular pulse of support T_c/N_{B2} and unit amplitude. In eqn. 1, the first term comes from the SinBOC(1,1) modulated code (held at rate $12/T_c$ in order to match the rate of the second term), and the second term comes from a SinBOC(6,1) modulated code.

In TMBOC implementation, the whole signal is divided into blocks of N code symbols and $M < N$ of N code symbols are SinBOC(1,1) modulated, while $N - M$ code symbols are SinBOC(6,1) modulated. Using similar derivations as in [7], we can obtain the formula for TMBOC waveform. An equivalent unified model of CBOC and TMBOC modulations can be derived using the facts that $M, N \ll \infty$ and that, since w_1, w_2 are amplitude coefficients and $M, N - M$ define the power division between SinBOC(1,1) and SinBOC(6,1), we may set the following relationship between w_1, w_2 and M, N : $w_1 = \sqrt{\frac{M}{N}}$ and $w_2 = \sqrt{\frac{N-M}{N}}$. Therefore, in accordance with [8], the unified model can be written as:

$$\begin{aligned} s_{MBOC}(t) &= w_1 c_\delta(t) \otimes s_1(t) \otimes p_{T_{B2}}(t) \\ &+ w_2 c_\delta(t) \otimes s_2(t) \otimes p_{T_{B2}}(t) \end{aligned} \quad (3)$$

where $\delta(\cdot)$ is the Dirac pulse, \otimes is the convolution operator, $c_\delta(t)$ is the code signal without pulse shaping:

$$c_\delta(t) = \sqrt{E_b} \sum_{n=-\infty}^{\infty} b_n \sum_{m=1}^{S_F} c_{m,n} \delta(t - nT_c S_F - mT_c), \quad (4)$$

and $s_1(t), s_2(t)$ are SinBOC-modulated parts (with associated hold block when needed), given by:

$$s_1(t) = \sum_{i=0}^{N_{B1}-1} \sum_{k=0}^{N_{B2}-1} (-1)^i \delta \left(t - i \frac{T_c}{N_{B1}} - k \frac{T_c}{N_{B2}} \right), \quad (5)$$

and, respectively:

$$s_2(t) = \sum_{i=0}^{N_{B2}-1} (-1)^i \delta \left(t - i \frac{T_c}{N_{B2}} \right) \quad (6)$$

In our simulations, we use MBOC modulation with $M = 10$ and $N = 11$, since MBOC combines SinBOC(6,1) and SinBOC(1,1) spreading symbols with a 1/10 average power ratio. In [8], the equivalence between CBOC and TMBOC implementations was discussed.

III. MULTI-CORRELATOR STRUCTURE AND THEIR CLOSED LOOP IMPLEMENTATION

Compared with the conventional EML tracking loop, where only 3 correlators are used (i.e. Early, Prompt and Late), here, in the multi-correlator based structure, we generate a bank of correlators (in this implementation, we use 81 correlators with 0.05 chips spacing between successive correlators) as presented in Fig. 1. This large number of correlators is needed in order to include the feed-forward techniques in the comparison, because feed-forward techniques make use of these correlators for estimating the channel properties while taking decision about the code delay [1]. Some of these correlators can be kept inactive or unused, for example when EML and HRC tracking loops are used. After the necessary front-end processing, and after the carrier has wiped-off, the received post-processed signal is passed through a bank of correlators. As shown in Figure 1, the NCO and PRN generator block

produces a bank of early and late versions of replica codes based on the delay of the Line-Of-Sight (LOS) signal $\hat{\tau}$, the correlator spacing Δ , and the number of correlators N . In case of EML tracking loop, the corresponding early-late spacing is equal to 2Δ . The received signal is correlated with each replica in the correlator bank, and the output of the correlator bank is a vector of samples in the correlation envelope. Therefore, we

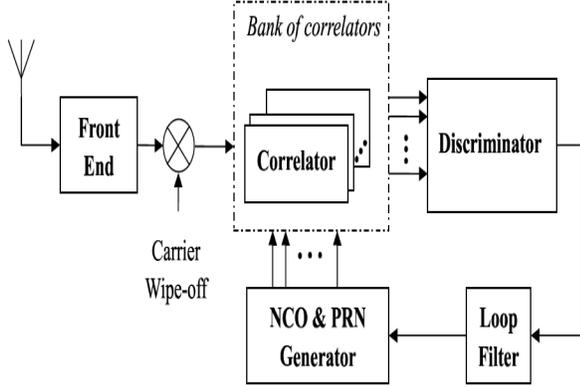


Fig. 1. Block diagram for multi-correlator based DLL implementation

obtain the correlation values for the range of $\pm N\Delta$ chips from the prompt correlator, where N is the number of correlators and Δ is the correlator spacing between successive correlators. The various code tracking algorithms (named as Discriminator in Fig. 1) utilize the correlation values as input, and generate the estimated LOS delay as output, which is then smoothed by a loop filter. In accordance with [6], the implemented code loop filter is a 1st order filter, whose function can be written as:

$$\hat{\tau}(k+1) = \hat{\tau}(k) + \gamma d(k) \quad (7)$$

where γ is calculated based on loop filter bandwidth, B_n . A DLL loop bandwidth of 1 Hz is used, assuming that carrier aiding is always available [10].

IV. MULTIPATH ERROR ENVELOPES IN CLOSED LOOP MODEL

The multipath tracking performance is studied first via Multipath Error Envelopes (MEE) for static 2 path channel in closed loop model. In MEE analysis, the noise free environment is always considered, and the focus is on the multipath induced delay errors. Typically, a two path model is used to generate the MEE curves as in [3], [10]. The multipath amplitude is 3 dB less than the LOS path amplitude. The MEE curves are obtained for two extreme phase variations (i.e., 0 and 180 degrees) of multipath signal with respect to LOS component. The multipath delays are varying from 0 to 1.5 chips with a step size of 0.05 chips. The MEE simulations

were carried out for BPSK, SinBOC(1,1) and MBOC modulated signals. A more representative curve known as Running Average Error (RAE) is presented here in accordance with [3]. RAE is computed from the area enclosed within the multipath error and averaged over the range of the multipath delays from zero to the plotted delay values.

Four different code tracking algorithms are analyzed for performance comparison. Among them, nEML and HRC are conventional delay tracking algorithms. The narrow correlator or nEML is the first approach to reduce the influence of code multipath that uses a chip spacing of 0.05 or 0.1 chips (less than 1 chip) depending on the available front-end bandwidth [2]. HRC uses an increased number of correlators (i.e. 5 correlators) for better coping with medium-to-long delay multipath [5], [10].

The other two algorithms are based on feed-forward technique, known as Peak Tracking based on 2nd order Differentiation (PT(Diff2)) and Teager Kaiser (TK). Both of these algorithms utilize the adaptive threshold computed from the estimated noise variance of the channel in order to decide on the correct code delay [1]. These feed-forward algorithms first generate competitive peaks which are above the computed adaptive threshold. In PT(Diff2), the competitive peaks are then multiplied by some optimized weighting factors, which are assigned based on the peak power, the peak position and the delay difference of the peak from the previous delay estimate. Finally, PT(Diff2) selects the peak which has the maximum weight as being the best LOS candidate. In contrast to PT(Diff2), TK only considers the delay difference of the peak from the previous delay estimate while taking decision on the LOS delay [1].

Running average error results for BPSK, SinBOC(1,1) and MBOC modulated signals are shown in Figs 2, 3 and 4, respectively. For all three different types of modulation, PT(Diff2)

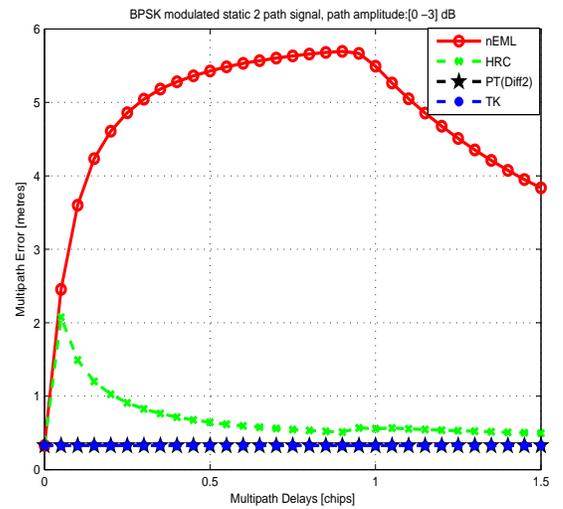


Fig. 2. Running Average Error for BPSK modulated static 2 path signal and TK can mitigate the multipath effects completely for

all possible multipath delays in the absence of noise. The simulation results in the presence of noise are to be shown in Section V. Their curves are overlapping in Figs 2-4. Among the other two algorithms, HRC outperforms nEML in all three cases as observed from the RAE curves. The multipath improvement of MBOC signal over SinBOC(1,1) signal can be noticed for nEML and HRC. This is mainly because of the fact that MBOC modulated signal has slightly steeper auto-correlation main lobe, as opposed to the auto-correlation main lobe of SinBOC(1,1) signal.

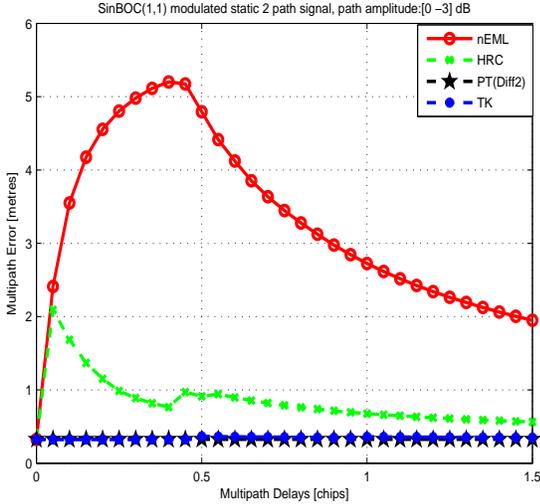


Fig. 3. Running Average Error for SinBOC(1,1) modulated static 2 path signal

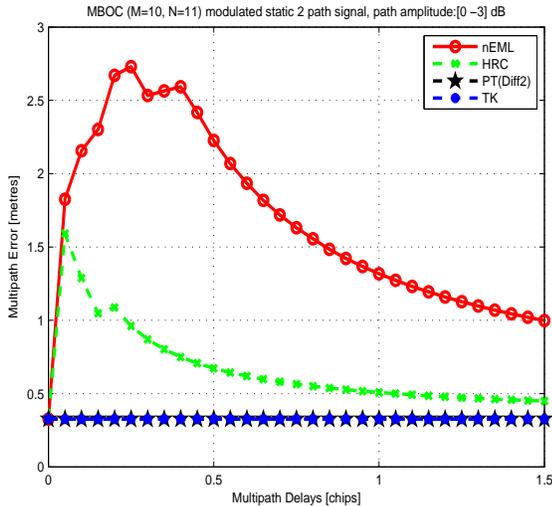


Fig. 4. Running Average Error for MBOC ($M = 10$, $N = 11$) modulated static 2 path signal

V. SIMULATION RESULTS IN CLOSED LOOP MODEL

Simulations were carried out in closely spaced multipath scenarios for BPSK, SinBOC(1,1) and MBOC modulated

signals. The simulation profile is summarized in Table I. The number of channel path was fixed to 2 with random path separation between 0.05 and 0.5 chips and the path phases were randomly distributed between 0 and 2π . The channel paths were assumed to obey a decaying Power Delay Profile (PDP), where the amplitude of the second path α_2 is exponentially decaying with respect to the amplitude of the first path α_1 and to the path separation: $\alpha_2 = \alpha_1 e^{-\mu x_{max}}$, where x_{max} is the path separation and μ is a path decaying coefficient (here $\mu = 0.04$ chips). The received signal was sampled at $N_s = 20, 10$ and 2 for BPSK, SinBOC(1,1) and MBOC modulated signals, respectively. Low N_s was chosen intentionally for faster execution of the simulations and N_s varies from one modulation to another in order to have the same number of samples per chip for all three cases.

TABLE I
SIMULATION PROFILE DESCRIPTION IN CLOSED LOOP MODEL

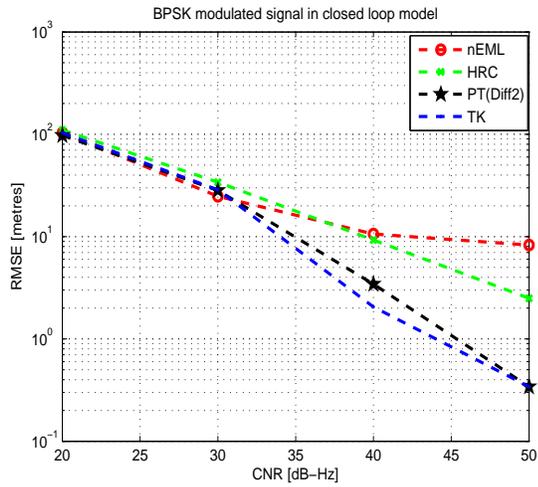
Parameter	Value
Path Profile	2 path static channel
Path Power	Decaying PDP with $\mu = 0.04$ chips
Path Spacing	Random between 0.05 and 0.5 chips
Path Phase	Random between 0 and 2π
Correlator Spacing	0.05 chips
Number of Correlators	81
Coherent Integration, N_c	20 msec
Non-coherent Integration, N_{nc}	1 block
Oversampling Factor, N_s	[20, 10, 2]
NCO Loop Bandwidth	1 Hz
Loop Filter Order	1 st order
Bandwidth effect	No
Number of Random Realizations	$100 * 20 = 2000$

The statistics were computed for $N_{rand} = 2000$ random realizations for each particular Carrier-to-Noise-Ratio (CNR). The Root-Mean-Square-Errors (RMSE) are plotted in metres, by using the relationship $RMSE_m = RMSE_{chips} c T_c$; where c is the speed of light, T_c is the chip duration, and $RMSE_{chips}$ is the RMSE in chips.

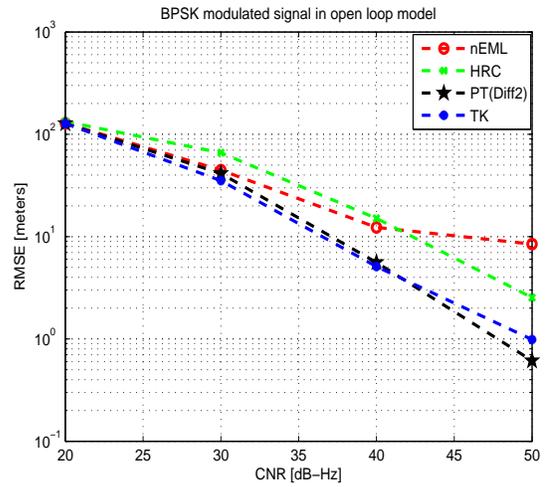
RMSE vs. CNR plots for all three modulations are shown in Fig. 5. The feed-forward algorithms showed superior performance for all three modulation types in moderate-to-high CNR conditions (i.e., from around 30 dB-Hz onward). Among the other two algorithms, HRC shows better multipath mitigation capability than nEML only in good CNR conditions, since HRC is more sensitive to noise.

VI. SIMULATION RESULTS IN OPEN LOOP MODEL

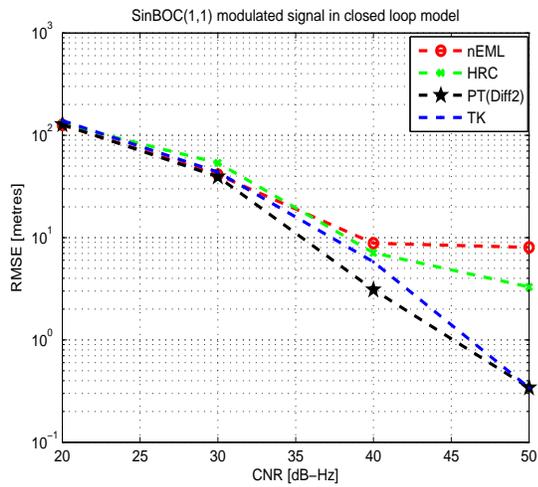
Simulations were carried out in the same simulation environment as mentioned in Table I, but in open loop configuration, i.e., without any NCO and loop filter, having one shot estimates. RMSE vs. CNR plots for open loop model are shown in Fig. 6. As in closed loop implementation, here also, the feed-forward algorithms perform the best for all three modulation types in moderate-to-high CNR conditions. Similar conclusion (as like closed loop implementation) can be drawn for HRC and nEML.



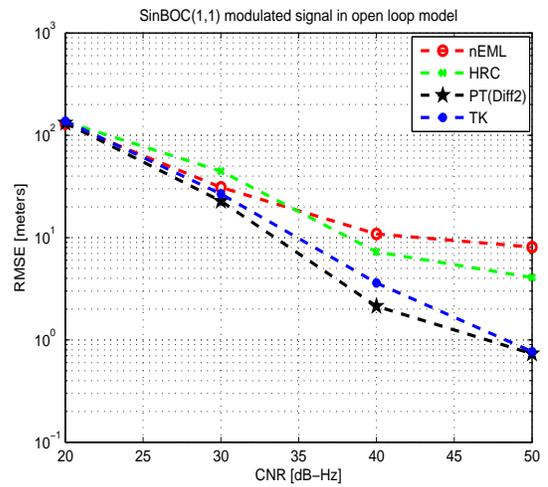
(a) BPSK signal



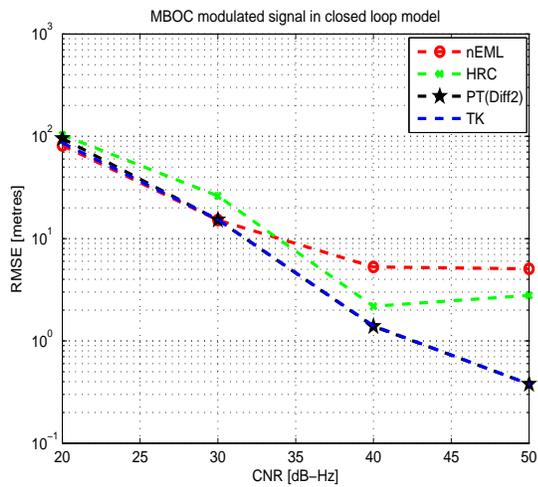
(a) BPSK signal



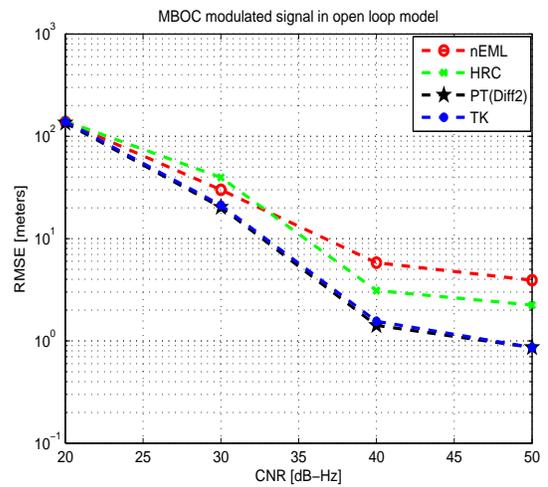
(b) SinBOC(1,1) signal



(b) SinBOC(1,1) signal



(c) MBOC signal



(c) MBOC signal

Fig. 5. RMSE vs. CNR plots for in closed loop model

Fig. 6. RMSE vs. CNR plots for in open loop model

VII. CONCLUSIONS

In this paper, the feed-forward techniques, previously proposed by the authors in [1], are implemented in closed loop configuration. The multipath performance of these algorithms along with the conventional DLLs for three different modulation types, including the newly proposed MBOC modulation, are presented in terms of running average error and RMSE. The results are then compared with the open loop counterpart for observation. It is shown that the feed-forward algorithms provide much better multipath mitigation performance than the traditional DLLs in moderate-to-high CNRs for all three modulated signals in both closed and open loop models. It is also shown that the results in closed loop and open loop configurations are similar (closed loop results slightly better), which points out the fact that the feed-forward algorithm analysis can be quite conveniently (i.e., faster) asserted in open loop configuration. The multipath improvement of MBOC signal over BPSK and SinBOC(1,1) signals is also evident from the simulation results (i.e. smaller RMSE values for MBOC signal as compared to other two signals).

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