Architectures and Codecs for Real-Time Light Field Streaming

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Abstract

Light field 3D displays represent a major step forward in visual realism, providing glasses-free spatial vision of real or virtual scenes. Applications that capture and process live imagery have to process data captured by potentially tens to hundreds of cameras and control tens to hundreds of projection engines. Light field displays are capable of making up the human perceivable 3D light field using an individual computing node. We therefore analyze the bottlenecks and data flow of the light field conversion process and identify possibilities to introduce better scalability. Based on this analysis we propose two different architectures for distributed light field processing. To avoid using uncompressed video data all along the processing chain, we also analyze how the operation of the proposed architectures can be supported by existing image / video codecs.

Introduction

Three dimensional (3D) displays [1][2] represent a new class of terminal devices, that make a major step forward in realism towards displays that can show imagery indistinguishable from reality. 3D display technologies use different approaches to make the human eyes see spatial information. While the most straightforward approaches use glasses or other head-gear to achieve separation of left and right views, autostereoscopic displays achieve similar effects without the necessity of wearing special glasses. Typical autostereoscopic display technologies include parallax barrier [3], lenticular lens [4], volumetric [5], light field [6][7] and pure holographic [8] displays, most of them available commercially, each with its unique set of associated technical challenges. Projection-based light field 3D displays [9][10][11] represent one of the most practical and scalable approaches to glasses-free 3D visualization, achieving, as of today, a 100 Mpixel total 3D resolution and supporting continuous parallax effect.

Various applications where 3D spatial visualization has added value have been proposed for 3D displays. Some of these applications involve displaying artificial / computer generated content, such as CAD design, service and maintenance training, animated movies and 3D games, driving and flight simulation. Other applications require the capturing, transmission / storage and rendering of 3D imagery, such as 3DTV and 3D video conferencing (3D telepresence). From the second group, applications that require live 3D image transmission are the technically most demanding, as 3D visual information has much more information content compared to its 2D counterpart. In state of the art 3D displays, total pixel count can be 1-2 orders of magnitude higher than in common 2D displays, making such applications extremely data intensive.

We focus on the problems associated with applications that require real-time streaming of live 3D video. We consider projection-based light field 3D displays as the underlying display technology (with tens or hundreds of projection engines) coupled with massive multi-camera arrays for light field capturing (with tens or hundreds of cameras). We explore possibilities that can potentially work in real-time on today’s hardware. Please note that the majority of the problems discussed here also apply to other 3D capture and display setups in which the necessary data throughput cannot be handled by a single data processing node, and as such distributing the workload becomes necessary. Our novel contribution presented in this paper lies in: analysis of the typical data flow taking place during converting multi-view content to display specific light field representation; analysis of bottlenecks in a direct (brute force) light field conversion process; presentation of two possible approaches for eliminating such bottlenecks; and a suitability analysis of existing image and video codecs for supporting the proposed approaches.

The paper is organized as follows. First light fields and light field displays, as well as different content generation and rendering methods for light field displays are introduced. Then the proposed architectures for scalable light field decompression and light field conversion are described, followed by a comparative analysis of the discussed architectures and codecs.

Light Fields, Light Field Displays and Content Creation

The propagation of visible light rays in space can be described by the plenoptic function, which is a 7D continuous function, parameterized by location in 3D space (3 parameters), angles of observation (2 parameters), wavelength and time [12]. In real-world implementations, a light field, which is a simplified 3D or 4D parameterization of the plenoptic function, is used, which allows to represent visual information in terms of rays with their positions in space and directionality [13][14]. In a 4D light field, ray positions are identified by either using two planes and the hit point of the light ray on both planes, or by using a single hit point on a plane and the direction to which the light ray propagates. This 4D light field describes a static light field in a half-space. For a more detailed description, the reader is referred to [15].

Having a light field properly reproduced will provide the user with 3D effects such a binocularity and continuous motion parallax. Today’s light field displays can typically reproduce a horizontal-only-parallax light field, which allows the simplification of the light field representation to 3D.
The previously described distributed optical system is served by a parallel rendering system, implemented as a cluster of multi-GPU computers. In the simple case, each GPU output drives a single optical module, multiple outputs of the same GPU correspond to adjacent optical modules, while one computer (having multiple GPUs) corresponds to a bigger group of adjacent modules. As in [18], we define the part of the whole light field that is reproduced by a single optical module a light field slice. One such slice is not visible from a single point of observation, nor does it represent a single 2D view of the scene. The set of light rays contained in such a slice is determined by the optical setup of the display and is calculated during the design process.

The cluster nodes are connected through a network (for example Gigabit Ethernet, Infiniband or 40 Gbit Ethernet), which carries all the data to be rendered / visualized, unless it is pre-computed and stored locally in the rendering nodes. The data received from an external source first needs to travel through the network, in the RAM of the rendering nodes, uploaded to and processed by the GPUs, output on a video interface, and finally displayed by the optical modules. The fastest light field conversion technique currently employed in light field rendering software is interpolation of light rays from the nearest samples. All data that contributes to the calculation of the color of a specific outgoing light ray thus needs to be available in the GPU responsible for that light ray.

### Content Creation for Light Field Displays

For the sake of achieving the highest possible light field quality on the display side, we prefer to use a dense set of input views, so that no depth estimation and virtual view synthesis is necessary to perform the light field conversion, thus the rendering process involves light field interpolation only. This approach has been successfully used on many occasions for light field displays resulting in crisp and artifact-free images regardless of scene complexity, material properties or visual effects involved. Therefore this seemingly brute force approach is preferred also for live imagery, so that any type of scene can be handled with no view synthesis artifacts.

While this requires a high number of capture cameras, installing such a rig of low cost cameras is feasible in fixed settings (such as a film studio or a video conferencing room). Installing one for capturing events, while more demanding, is also possible and is done commercially [19] as well as for experimental projects (although most of these target free viewpoint viewing and are not necessarily aware that the captured input can also be used as a light field).

If the captured light field is to be transmitted over longer distances, or stored, it is absolutely necessary to compress it due to the large volume of data. It might even be necessary to compress the video streams captured by the cameras (preferably inside the cameras) to make the data throughput manageable. While compressing tens or even hundreds of views may seem counterintuitive due to reasons of bandwidth requirements, recent results have shown that compressing a 80-view XGA resolution light field stream (while keeping good visual quality) is not much more demanding than compressing a 4K video signal in terms of consumed bandwidth [20]. It has to be noted that the complexity of compressing or even decompressing this many views using the technology discussed in that paper does not allow for real-time operation today.

One of the most well known camera arrays for capturing light fields is the Stanford Multi-Camera Array [21], consisting of 128...
video cameras that can be arranged in various layouts, such as a linear array of parallel cameras or a converging array of cameras having horizontal and/or vertical parallax. Numerous other multi-camera setups have been built since then for both research and commercial purposes, e.g. the 100 camera system at Nagoya University [22], the 27-camera system at Holografika [23] (discussed later in this paper) or the 30-camera system from USC Institute for Creative Technologies [24]. These camera systems capture a sufficiently dense (in terms of angular resolution) and wide (in terms of baseline) light field, so that the captured data can be visualized on a light field display without synthesizing additional views beforehand.

One can also use a single moving camera for capturing a static scene [25], or a static camera with a rotating object [26] to obtain a light field. As the last two approaches capture a static light field, they are not in the scope of the discussion presented in this paper, which is about processing light field video. Also please note that plenoptic, range and other single-aperture capture methods are not discussed in this paper, as those cannot capture a scene from a wide viewing angle, unless the captured scene is extremely close (small).

When imagery is derived from a geometrical representation (that is, rendered from a 3D model), the resulting 2D image is a projection of the 3D scene. Synthesizing 2D views from many viewing angles is relatively straightforward in this case, as creating an array of synthetic cameras and rendering additional images from those is a matter of scripting in most modeling tools such as 3ds Max, Maya or Blender. As such, rendering a dense set of views to serve as light field content is only challenged by increased rendering time, and potentially 2D-only visual effects applied in the rendering pipeline which do not work consistently across multiple views. The practicality of this approach has been demonstrated with the Big Buck Bunny light field sequences [27], or the San Miguel sequence [28]. Densely rendered sequences can be used for light field displays without synthesizing additional views. When rendering synthetic scenes, it is also relatively easy to extract ground truth depth information, as depth maps are commonly used during rendering. Thus, depth information is available as a byproduct of the process.

There are attempts to capture and represent LF content with sparser camera views while estimating the geometry (depth) of the scene and augmenting the available views with depth maps to be used for subsequent intermediate view interpolation (synthesis) in order to provide the missing rays. Many techniques have been proposed for rendering intermediate views from a sparse set of views [29], as well as rendering extrapolated views from narrow angle content [30]. Most of these techniques are rooted from stereo matching for depth estimation, and depth based view synthesis by pixel shifting [31] or warping [32], and improving the resulting views by hole filling [33], inpainting [34], and other techniques. As soon as these techniques can provide sufficient quality and work in real-time to generate the rays needed for the display based on live imagery, they will be used in LF displays.

Light fields conversion is possible without explicit geometry / depth information about the scene. The simplest technique involves resampling the light field, which can be implemented by interpolating the 4D light field function from the nearest samples [14]. More involved is the lumigraph approach [13], where a rough geometric model is obtained, which is then used to improve the quality of the rendered lightfield by defining the depth of the object being rendered, thus allowing for better ray space interpolation.

When it comes to live imagery, dense image-based techniques, while requiring a massive amount of images captured or rendered, result in the highest possible light field quality. This dense set of input views is easily converted to display-specific LF slices, however at the expense of getting the large number of input pixels to be available in the display’s rendering nodes. The sheer amount of data necessitates novel streaming architectures and codecs that are able to handle such imagery.

Light Field Streaming

When performing light field streaming from an array of cameras, the simplest approach is to have all images captured by the cameras transferred to the GPUs of all rendering nodes to make sure all pixels that might be required during the light field conversion process are immediately available. This brute force approach has previously been implemented and described in [23], and illustrated in Fig. 2. In that system, a linear 27-camera array was used to feed a light field display. The cameras performed MJPEG compression of the images in hardware, which have been transferred to all rendering nodes simultaneously. The rendering nodes decompressed all 27 MJPEG images in parallel, and performed light field conversion on their respective light field slices on GPU. The performance of the whole system reached 15 FPS with 640x480 images, and 10 FPS with 920x720 images back in 2009.

The brute force approach outlined above has several possible bottlenecks. First, the time necessary to decode all the JPEG images (either on CPU or GPU) increases linearly with the number of images (that is, number of cameras), and the resolution of the images. Second, the network bandwidth required to transfer all the compressed images to the rendering nodes increases linearly with the number and resolution of the images. Third, the memory required to store the uncompressed images increases linearly with the number and resolution of captured images (both CPU and GPU memory).

If we consider an 80-view input (we used the BBB light field test sequences [27]), each view with 1280x768 resolution and 24 FPS, uncompressed, then the bandwidth required to transmit all pixels is 45.3 Gbit/s, which is difficult to reach with common network technologies.

Considering the same input, using 1:10 ratio JPEG compression, still gives a total bandwidth requirement of 4.5 Gbit/sec and an averaged PSNR of 44.65 dB calculated over the luminance channel. While it is possible to transmit this amount of data through a 10- or 40-Gigabit Ethernet channel in real time, it is not feasible to decompress and process it. Using the fastest available libjpeg-turbo JPEG decoder library [35], and a 6-core i7-5930K CPU @ 3.5 Ghz, one can decode 80 such views in ~43 ms.
If the system performs JPEG decoding only, this results in ~23 FPS decoding speed, and this does not even include any further processing or rendering. Achieving higher resolution or processing a wider or denser set of views is clearly limited by hardware, and thus not scalable.

If we choose H.264 instead of JPEG for compressing the views using the x264 encoder [36] and maintaining a similar PSNR with QP=20 (QP: Quantization Parameter, which determines image quality versus bandwidth in the video encoder), and use ffmpeg [37] to decode the 80 views on the same CPU in parallel, we can reach ~16.94 FPS for decoding all views. It this measurement we used a RAM disk to avoid any significant I/O overhead.

An efficient implementation of the light field conversion process is using weighted look-up tables that map multiple pixels originating from views to an outgoing light ray, with the weighting appropriate for the given light ray. The pixel correspondences stored in the look-up table are calculated based on the geometry of the captured views (intrinsic and extrinsic parameters) and the geometry of the optical modules (display design, calibration data).

The light field conversion process only needs to look up and blend the necessary view pixels to generate an outgoing light ray. As such, as long as the capture configuration, display configuration and mapping between them does not change, the look up tables remain constant, the same pixel positions are used while generating the same set of light rays.

In the light field conversion algorithm used with HoloVizio displays today, which uses a variant of the light field conversion algorithm described in [14], a maximum of two captured pixels contribute to one outgoing light ray. That is, the maximum number of light rays used by a rendering node cannot be more than twice the number of light rays generated by the same node, regardless of the number of total pixels captured. Take a HoloVizio 722RC display with 72 optical modules as an example, served by 6 rendering nodes, each rendering adjacent light field slices for 12 optical modules. Consider again an 80-view input, each view with 1280x768 resolution. Checking which pixels of a specific view are used by each rendering node, we can realize that rendering nodes that drive adjacent optical modules use slightly overlapping parts of the view (see Fig. 4). However, rendering nodes that are driving optical modules positioned further from each other use a disjoint set of pixels from the input view. None of the other pixels of this view contribute to the final image. These pixels do not have to be transmitted to the renderer. It may even happen that a specific rendering node does not use any pixels of a specific view. In this case, dropping the view on the specific rendering node would not have any effect on the final image.

These properties of the typical data access patterns of light field conversion can be exploited to make the system more scalable, and eventually achieve real-time operation regardless of the number and resolution of the incoming views. Two proposed solutions are described next.

**Streaming Architectures and Codecs**

### Two-Layer Architecture

One possibility to scale the decompression workload without introducing any new bottlenecks is to introduce two layers in the system: one for decoding the incoming streams in parallel, and a second one to perform the light field conversion (see Fig. 5). The nodes in the first layer decompress the video streams in a way that the decoding workload is distributed equally (so that given N views and M nodes, one node decodes N/M video streams). The number of nodes in the first layer should be chosen so that given the processing power of each node, we have enough nodes to decompress all the individual video streams in real time and transmit them to the second layer.

![Figure 3. A set of perspective views depicting the same scene are considered as a display independent light field representation, as it can be used to visualize the light field on any suitable 3D display. The images required by a specific display's projection modules are referred to as light field slices, and the set of light-field slices are therefore referred to as a display specific light field.](image-url)

Clearly, such a system having decoding time linear with the number of views and total pixel count cannot scale beyond a specific number of input cameras and resolution, given a specific hardware configuration. While the hardware can be upgraded to some extent, the processing power of a single node cannot be increased indefinitely (nor is it economical).

While increasing the number of cameras and/or the resolution of these cameras increases the quality of the generated light field, the number of rendered light rays remains constant. It seems counterintuitive to use an increasing amount of input data to generate the same amount of outgoing light rays, and, as we will see in the next sections, this can be avoided.

### Light Field Conversion and Data Access Patterns

Light field conversion transforms many input 2D views (which can be considered as a display independent light field representation) into light field slices, as required by the optical modules of the targeted light field display. One such light field slice is composed of many outgoing light rays (for example, 1024 x 768 pixels in case of an XGA optical module), which are in turn interpolated from pixels from many input views. Starting from views, we can also see that one input view typically contributes to many light field slices. Fig. 3 shows that views typically contribute to many adjacent light field slices, and that light field slices are typically composed of pixels originating from many adjacent views.

![Figure 4. Left: Adjacent rendering nodes consume adjacent, slightly overlapping parts of a source view. Red, green and blue overlays represent the areas of the image used by three rendering nodes that drive adjacent optical modules. Right: Rendering nodes that drive optical modules positioned further away from each other use a disjoint set of pixels from the same source view.](image-url)
It is easy to see that in this case the processing power of the individual nodes can be chosen arbitrarily – as long as one can decode at least one video stream, the number and processing power of the nodes can be traded off. The second layer requests uncompressed pixels from the first layer through a high-speed network. As we have seen before, the number of requested pixels can be maximum twice the number of pixels generated by a specific node. In case of driving 12 1280x800 resolution optical modules, this can be up to 1.41 Gbits/second, assuming 24 FPS, which is in the manageable range inside a rendering cluster. To avoid the collection and transfer of single pixels, we can transfer rectangular blocks which form the bounding box of the required pixels. Due to the compact shape of the required image regions, a bounding box does not add too much extra pixels to transmit, but has the benefit of being continuous in memory.

All rendering nodes require a different set of pixels, thus each will request the transmission of different, slightly overlapping regions. As the regions are fixed if the camera settings, display parameters and the mapping between them are fixed, the rendering nodes can subscribe to receive these fixed image regions on each new frame. The bandwidth required for transmitting and receiving the decompressed image regions can be scaled with appropriately choosing the number of processing nodes in both layers.

While the first layer can be connected to the data source using a network necessary to transmit the compressed video, the first and second layers are connected using a high-speed network to be able to carry the uncompressed image regions.

If the selected network architecture can take advantage of transferring large chunks of continuous in-memory blocks more efficiently than doing a large number of small transfers (for example InfiniBand RDMA [38]), then the images can be stored rotated with 90 degrees in memory, so that full height vertical blocks become a continuous memory block, assuming the common line-by-line interleaved image storage format.

In order to reduce the network load between the first and second layer, it is possible to employ a slight, fixed ratio compression method that can be decompressed even when only partially transmitted, such as the S3TC / DXTn texture compression method that is available on virtually all GPUs.

**One-Layer Architecture with Partial Decoding**

The second proposed architecture does not require two processing layers, as decoding and light field conversion happens on the same processing nodes (see Fig. 6).

In this case however, decoding needs to happen in real time regardless of the number of views to be decoded. To achieve that, we can take advantage of the fact that each renderer is using only portions of the views, as shown before. As we have seen before, the main bottleneck is decompression of all parts of all views, even those that are finally left unused in the specific rendering node. Therefore, assuming that we can save time by skipping the decoding of some views, and also skipping the decoding of image regions that are not needed, we can decode only those image regions which are necessary on the current rendering node. Using this approach, the decoding workload can be reduced with different codecs, if certain conditions are met.

This, however, poses unusual requirements on the image / video codec used for the transmission of the views. In the following subsections, we analyze common image / video codecs from this perspective: JPEG, JPEG2000, H.264 and HEVC. In the case of JPEG and JPEG2000, frames are encoded independently. In the case of H.264 and HEVC, we enable inter frame compression.

In a horizontal only parallax light field display, the image regions are typically vertical. H.264, HEVC and JPEG2000 support vertical slice shapes, while in the case of JPEG, which supports only horizontal subdivision, rotating the image before encoding is necessary.

The configuration used for all tests described below is a 6-core i7-5930K CPU @ 3.5 Ghz PC with a GeForce GTX 960 GPU with 2GB RAM, and all I/O performed on RAM disk.

The experiments were performed on the Big Buck Bunny light field sequences [27], more specifically the Flowers scene. Other content (i.e. Balloons sequence [39] in Fig. 9 and Fig. 10) is used only for illustration purposes.

**H.264**

H.264 [40] can partition a full frame into regions called slices, which are groups of macroblocks (please note these are different from light field slices). An encoder is commonly configured to use a single slice per frame, but can also use a specific number of slices (see Fig. 7). This configuration may also specify the way macroblocks are partitioned into slices. Slices are self-contained in the sense they can be parsed from the bitstream without having the other slices. Slicing originally serves the purposes of providing error resilience and also to facilitate parallel processing. Slicing introduces some increase in bitrate, but this is minor compared to the overall bitrate, as seen in Fig. 8.
Figure 7. A frame subdivided into horizontal H.264 slices. In an I-frame, these slices can be decoded independently.

Figure 8. Bitrate increase caused by slicing a video frame into an increasing number of slices, shown for different QPs.

To see how H.264’s slicing feature can support partial decoding of video streams, we need to consider how it is implemented in the encoder and decoder. In H.264 we can differentiate Intra frames (I-frames) which are encoded independently from other frames in the video sequence, and hence employ prediction only inside the frame. Predicted frames (P-frames) and Bidirectionally predicted frames (B-frames) use image information of previously encoded / decoded frames in the encoder / decoder, exploiting similar blocks of pixels in subsequent frames moving across the image in time, typically representing a moving object.

When using multiple slices, encoders disable intra-frame prediction across slice boundaries when encoding I-frames. Therefore it is possible to decode only parts of an I-frame by decoding the respective slices and skipping the other slices (see Fig. 9). On the other hand, when performing inter-frame prediction in P-frames and B-frames, all encoders we have checked disregard slice boundaries, and perform motion prediction across slice boundaries. This means that the decoding process will also assume that the frame to be used for motion compensation is fully available in the decoder. If, due to partial decoding of the previous frames, this condition is not met, the image regions that correspond to skipped slices will contain bogus color in the Decoded Picture Buffer.

Figure 9. When using I-frame only encoding in H.264, dropping a slice has no effect on the remaining parts of the image. This image has been subdivided into three slices, and the middle slice dropped prior to decoding.

Subsequently the motion compensation process will copy bogus colors from the buffer to the image being decoded, whenever a motion vector goes across the boundary of a missing slice (see Fig. 10). When such an erroneous decoded frame is used as a basis of motion compensation, the error further propagates from the undeclared regions as the decoder proceeds further in the stream. Only on the next I-frame the partially decoded image will be correct again, as the decoder does not use any other frames for decoding I-frames.

Therefore skipping the decoding of slices is possible only if the encoder is instructed not to perform motion prediction across slice boundaries (see Fig. 11). As this is not available as an option in any encoder we have evaluated, this functionality has been implemented by modifying the reference encoder JM 18.6 [41]. Our implementation is similar to that of [42], although serving a different purpose.
With restricting the motion vectors, we can achieve truly self-contained slices in the sense that decoding only selected slices becomes possible even for P- and B-frames, while still maintaining a standard confirming bitstream, though with a minor loss in coding efficiency due to partially restricting motion vectors. Implementation details have been described in our previous paper [43].

Once the encoded bitstream with self-contained slices is available at the decoder, there are several options to achieve partial decoding and thus saving decoding time. It is possible to modify the bitstream by droppingNAL units that correspond to the slices we do not wish to decode, like they were dropped by the network. While this results in a corrupt bitstream, some decoders (e.g. ffmpeg) can decode the remaining parts of the image, resulting in sub-linear speedup, as shown in Fig. 12. In this case, error resilience options have been disabled in the decoder to the extent possible, however we suspect that the missing slices in the bitstream still result in some decoding time overhead. Another option is to modify the decoder to explicitly skip the decoding of specific slices, which requires decoder modification (as this functionality was also unavailable in all decoders we have tested).

HEVC

There are many new coding features introduced in HEVC [44] compared to H.264, and even a short summary of these novelties is out of scope in this paper. Therefore we only focus on differences relevant for our use case, and we refer the interested reader to [45] for a summary of other changes and novel features.

Tiles in HEVC are a new concept [46], serving parallelization and packetization purposes. Tiles partition a video frame into a multiple number of rectangular partitions. Compared to slices, tiles provide more flexibility to partitioning and appear to incur less compression penalty since tiles do not contain any header. Furthermore, tiles are independent in terms of entropy coding, as the coder is re-initialized at the beginning of each tile.

In addition, intra-frame prediction is limited within tile boundaries. In the HEVC inter-frame prediction scheme, however, the tile boundaries are disregarded. Similar to the H.264 case, motion search has to be restricted to remain inside tile boundaries to ensure partial decoding in P- and B-frames. Tiling is thus suitable for enabling partial decoding, and this has been implemented in the HTM 14.0 reference software [47], alongside a tool for bitstream manipulation that removes selected tiles from the bitstream before decoding [48].

As the coded tiles may be interleaved with other coded data in the bitstream and as parameter sets and headers (e.g. slice segment header) are for the entire bitstream, a dedicated decoding process is defined for decoding particular tiles, while omitting the decoding of other tiles. For that, a tile-based extractor is designed to construct an HEVC full-picture-compliant bitstream corresponding to the desired tiles such that a standard decoder can cope with it.

JPEG

Images stored in JPEG [49] files are typically subdivided into 8x8 blocks (Minimum Coding Unit, MCU). The sequence of these MCUs is basically coded in two parts: the DC component, which is stored as a difference from the previous block, as well as the AC components, which are stored as a sequence of values in a specific ordering, block by block. As all the values and coefficients are Huffman coded, one needs to read all Huffman codes to be able to interpret the ones that are actually needed. Some steps, however can be skipped for the MCUs that are not needed, for example inverse DCT, dequantization, color conversion, etc. Unfortunately, Huffman decoding takes the major part of JPEG decoding time, according to our profiling of libjpeg-turbo. When forcing the decoder to skip all the steps after Huffman decoding for the unnecessary image portions, one can only gain a rather insignificant speedup, as shown in Fig 13.

There is however an optional feature in JPEG to facilitate separately entropy coded segments, called Restart intervals / Restart markers, which allow resetting the bitstream after every N MCUs, letting the decoder skip N MCUs at a time without decoding anything, by just looking for specific bytes. If the JPEG encoder can be instructed to use a frequent restart interval, this feature can facilitate fast skipping of unnecessary MCUs.
We have implemented this approach too, by modifying the libjpeg-turbo decoder to skip the unused MCU rows by skipping through restart markers, as well as skipping all other processing steps for those MCUs. The speedup in this case is significant, as shown on Fig 14. For example, by decoding only the quarter of an image, a 2.4x speedup can be gained. While this is still not linear, it is a nice addition to scalability.

The ordering of MCUs in the JPEG stream is fixed, therefore restart markers can be only used to create horizontal strips that can be skipped. If, like in our use case, vertical strips are necessary, then the images need to be rotated 90 degrees before encoding. Also, as mentioned earlier, the encoder needs to be set up to include restart markers at regular intervals, which might not be possible in every JPEG encoder (for example, JPEG capable cameras often have a compression quality settings, but no option for restart markers).

**JPEG 2000**

JPEG2000 [50] images can also be partially decoded. This functionality has been recently introduced in the proprietary Comprimato JPEG2000 codec [51], the CUDA-based version of which has been used for testing. Decoding 80 views takes 263 ms when decoding entire 1280x768 images, 8 bpp, 4:2:0 subsampling and default compression and decompression parameters, which accounts for 3.8 FPS operation taking only decoding into account.

On the other hand, when decoding only selected areas of each image, decoding time decreases significantly, although not strictly proportional with the image area, especially when decoding tiny image areas, as shown on Fig 15. Assuming that the application needs only the quarter of each image, a speedup of 54% can be observed, making decoding speed of 80 views 5.85 FPS. According to Comprimato, the fastest available GPUs at the time of writing can decode ~2.37 times faster, which corresponds to almost 14 FPS. Thus, very soon (after gaining a factor of two speedup over current GPUs) we can expect this solution to work in real time for the benchmark use case.

The main advantage of using JPEG2000 for this purpose is that no special considerations are necessary during encoding, and the partial decompression feature is already built in the decoder as an option.

**Comparative Analysis**

**Architectures**

The presented two-layer architecture is massively scalable, as the number of decoders, and the number of renderers can be chosen according to the performance of the individual nodes, while the bandwidth required by each rendering node is upper bounded by the number of pixels driven by each rendering node. Using this architecture, an arbitrary number of views with arbitrary resolution can be supported. On the other hand, the high speed network necessary between the two layers can make this setup quite costly in a practical case, as the required bandwidth is more than what can be provided by a Gigabit Ethernet network. Any codec can be used that can be decoded by the decoding layer in real time.

The one-layer architecture requires less number of processing nodes (as the separate decoding layer is eliminated) and does not need a high-speed network to connect the two nodes. On the other hand, to achieve real-time performance special considerations have to be taken in the video codec used for feeding the incoming views. The suitability of different codecs for this architecture is analyzed next.

**Codecs**

Our initial aim was to find a solution for real-time decompression of the (partial) views necessary for light field conversion.
Therefore our discussion mainly focused on the runtime performance of decoding, and its improvement when partial decoding is allowed. Here we directly compare the different solutions from this perspective. Also important is the bandwidth required by each codec to achieve same or similar image quality, therefore a brief analysis of bandwidth requirements is presented.

As described in before in detail, none of the evaluated codecs is capable of decoding image portions in a time proportional with the area to be decoded. While JPEG and JPEG 2000 show reasonable speedup, this speedup is not linear, therefore full scalability cannot be guaranteed for any number of views or any resolution, as scalability is only partial. In the case of JPEG, this scalability can only be exploited if the encoder supports it, and is configured appropriately.

It is apparent from Fig. 16 that JPEG is the clear winner when it comes to decoding performance, as even today, >24 FPS decoding can be achieved with using a publicly available codec with a modification to support partial decoding for the targeted use case. It comes as no surprise that the decoding performance advantage of JPEG comes at a cost in bandwidth (see Fig. 17). The bandwidth required by JPEG to achieve the same quality can easily be 10x higher than, for example H.264. JPEG2000 is in between the two in terms of bandwidth consumption. Please note this comparison of bandwidth requirements is by no means meant as a comprehensive comparison of codec efficiency, but just a rough guideline to see the order of magnitude difference between the codecs under consideration. For a more general comparison of different codecs, the reader is referred to works such as [52],[53],[54].

In all cases except JPEG2000, partial decoding capability requires some kind of subdivision of the images into independent regions, which increases the necessary bitrate. In case of JPEG, the difference is negligible (<0.2%). In case of H.264 and HEVC, the difference is bigger due to the restriction of motion vectors.

When restricting motion vectors in H.264 and HEVC, the more independent areas are defined, the smaller these areas will become, being even more restrictive in the selection of motion vectors. Smaller independent regions however result in less pixels decoded unnecessarily, and thus a possible higher decoding speedup.

That is, the granularity of the subdivision determines the tradeoff between bitrate increase and the compactness of the bounding rectangle that needs to be decoded to gain access to an arbitrary image region.

**Alternatives**

To avoid using special codec features and encoder / decoder side modifications, one may also choose a very simple alternative solution: subdivide the source videos into vertical stripes of specific size, and encode them separately, in parallel. This approach has been used in [55], and the authors show good results with this approach for the use case of panoramic video. This however requires even more bitrate than the solutions outlined above, as well as a large number of video streams to be handled synchronously.

Advanced codecs targeting multiview and 3D video, such as MV-HEVC and 3D-HEVC [56] could be used as well, provided real-time decoders would exist, but unfortunately this is not the case at the time of writing.

Scalado RAJPEG [57] is said to enable instant random access / partial decoding to images. However, RAJPEG was not made available for testing.

**Conclusion**

We have shown two different approaches to introduce scalability in real-time image-based light field based applications. While the two-layer approach can work with any codec (taking into account the necessary networking load), the one-layer architecture with partial decoding can only be achieved by modifying the way well known codecs typically work (except for the case of JPEG2000). While customization of codecs is possible to suit this requirement, the possibilities of forcing off-the-shelf hardware (for example a camera capable of providing compressed output) to use this custom codec are rather limited.

The special use of video codecs as outlined above indicates that current video compression technology is lacking an important feature. Therefore we have made steps [58],[59] to ensure that next generation video technologies have low-overhead random access among their requirements, and this has been accepted in the MPEG FTV requirements document [60]. This work was done in the hope...
that next generation video codecs will support this use case natively.

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References


P. Quax, F. Di Fiore, P. Issaris, W. Lamotte, F. Van Reeth, “Practical and Scalable Transmission of Segmented Video Sequences to Multiple Players using H.264,” in Motion in Games 2009 (MIG09), Lecture Notes in Computer Science LNCS series, LNCS 5884, pp. 256-267, 2009

A. Zare, P. T. Kovác, A. Gotchev, “Self-Contained Slices in H.264 for Partial Video Decoding Targeting 3D Light-Field Displays,” in Proc. 3DTV Conference 2015, Lisbon, 2015


Digital compression and coding of continuous-tone still images, ISO/IEC 10918-1, 1994

JPEG 2000 image coding system, ISO/IEC 15444, 2000


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